Atty.'s Docket No. 1662-20000 (1766 2426)
Applicants: Thomas J. BONOLA, et al
Title: Method & Apparatus for Eliminating the Software Generated
Ready-Signal to Hardware Devices that are not Part of the Memory
Coherency Domain
Sheet J of J Express Mail Label No. EV041360238US

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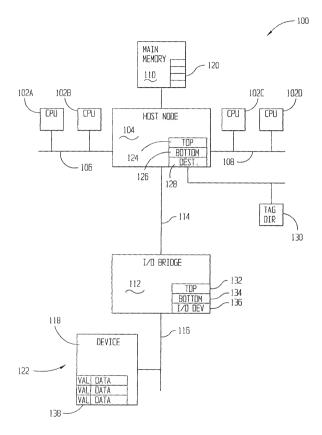


FIG 1

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